

FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE LIST OF REFERENCES CITED BY APPLICANT <i>(Use several sheets if necessary)</i>	ATTORNEY DOCKET NO. 1075.1174	APPLICATION NO. 1075.1174	FIRST NAMED INVENTOR Akio MATSUDA, et al.
	FILING DATE September 28, 2001	GROUP ART UNIT	1075.1174 U.S. PTO 09/28/01

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NO.	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
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—	AM	K. V. Rompaey, et al., "COWARE – A DESIGN ENVIRONMENT FOR HETEROGENEOUS HARDWARE/SOFTWARE SYSTEMS", (Proceedings EuroDAC), 1996
<u>KMN</u>	AN ✓	S. Vernalde et al., "An Object Oriented Programming Approach for Hardware Design ", (IEEE Computer Society Workshop on VLSI) 4/1999.
<u>KMN</u>	AO ✓	R. Cmar, et al., "A Methodology and Design Environment For DSP Asic Fixed Point Refinement", (Proceedings Design and Test in Europe Conference, pp. 271-276), 3/1999.

EXAMINER 	DATE CONSIDERED 2/4/05
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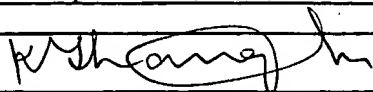
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KAN	AM ✓	P. Schaumont, et al., "Hardware Reuse at the Behavioral Level", (The Proceedings of the 36 th Design Automation Conference, ACM, 1999, pp. 784-789) June 1999.
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—	AN		A. Fukuda, "Parallel Operating System", pp. 30-32, Issued by Corona Publishing Co., Ltd., 1997
—	AO		S. Oaks, et al., "Java Thread Programming, pp. 1-6, Issued by Ohmsha, Ltd., 1997.

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